

**ABSTRACT OF THE DISCLOSURE**

A processor-based device includes a hot-plug capability which permits a peripheral device to be connected to the processor-based device without first removing power. The hot plug capability is implemented by a control circuit in the processor-based device which controls the application of power to the peripheral device in response to a device detect signal provided by the peripheral device when it is connected to the processor-based device. The control circuit also monitors the voltage applied to the power rail for the peripheral device. When the voltage on the peripheral device power level reaches a threshold, the control signal provides a signal indicating that the processor-based device may access the peripheral device.